



JPW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/507,213
Confirmation : 5089
Applicant : Dietmar BIRGEL
Filed : September 15, 2004
Title : Method for populating and soldering a circuit board,
reflow oven and circuit board for such a method
TC/A.U. : 1793
Examiner : M. Aboagye
Docket No. : BIRG3004/FJD
Customer No. : 23364

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22202-3514

Sir:

This is in response to the Official Action of October 18, 2007, in connection with the above identified application.

The Official Action is a restriction requirement in which it is urged that there are two separate and distinct inventions claimed in this application. Applicants elect the Group I invention which includes claims 31 - 53, 61 and 64, without traverse.

Applicants reserve the right to file one or more divisional applications on the non-elected inventions at a later time.

In view of the election of the Group I invention without traverse, an early and favorable action on the merits is now believed to be in order and is most respectfully requested.

Respectfully submitted,

Date: Nov. 5, 2007

Felix J. D'Ambrosio
Felix J. D'Ambrosio
Registration No. 25,721

BACON & THOMAS
625 Slaters Lane, Fourth Floor
Alexandria, Virginia 22314
Phone: (703) 683-0500

S:\Product\jpl\CLIENTS\Endress+Hauser Holding GmbH\BIRG3004 - EH 0543\Response to Restriction Req.wpd